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2012 IEEE International Symposium on Precision Clock Synchronization for Measurement, Control, and Communication

Plug-Fest September 23 – 25, 2012
Symposium September 26 – 28, 2012
San Francisco, CA, USA

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Aim and objective: The objective of the symposium, held at the San Francisco Airport Marriott Hotel, Burlingame, CA, is to provide a forum for researchers and practitioners from industry, academia, and government involved in the area of **Precision Clock Synchronization**.

Symposium topics: We welcome paper submissions on all areas of precision clock synchronization. Example topics of interest include:

<i>Design, usage and research concerning IEEE 1588</i>	<i>Performance test and evaluation tools</i>
<i>Implementation design and performance issues</i>	<i>Analytic, modeling, and simulation studies</i>
<i>System and device level studies and system design issues</i>	<i>System management</i>
<i>Servo design for slave clocks</i>	<i>System Security</i>
<i>Device design</i>	<i>Gigabit and fiber support, wireless networks</i>
<i>Components and embedded implementations</i>	<i>Applications and requirement investigations</i>
<i>Conformance testing and system integration issues</i>	<i>APIs, object models, programming models</i>
<i>Design environments and tools for applications</i>	<i>Work-in-Progress reports</i>

Plug-fest: The plug-fest is an opportunity to test and demonstrate device and system interoperability. Implementers are invited to cooperate with others and address compatibility issues in order to demonstrate system-wide clock synchronization and operation of protocols across a wide spectrum of devices. Prospective participants in the plug-fest should submit a letter of intent to participate, and an abstract of one page or less describing the devices, applications and the technical issues illustrated. Submission details are available at: <http://www.ispcs.org>.

Exhibitions: Promotional partner companies are invited to exhibit their products/prototypes. Exhibition space will be provided during the conference, allowing discussions with partners during the breaks. For details on becoming a promotional partner see: <http://www.ispcs.org>.

Submission of Papers: Authors should submit a six-page original manuscript. Templates and online submission of papers will be available at: <http://www.ispcs.org>. Students are welcome to submit papers.

Work in Progress Papers: Work in Progress (WIP) papers will be reviewed and presented in a poster session.

Paper Acceptance: All papers will be peer-reviewed. Each accepted paper must be presented at the conference by one of the authors. The final manuscript must be accompanied by the registration form and proof of payment. All conference and plug-fest attendees, including authors and session chairpersons, must pay the conference registration fee and their own travel expenses.

Final Papers: Final papers are limited to **six pages or less in IEEE double column format**. Full papers are accepted in IEEE compliant PDF format. All full papers will be published in the conference proceedings in CD and on IEEE Xplore.

Deadline for submission of regular papers

~~April 16, 2012~~ May 14

Letters of intent for plug-fest

May 14, 2012

Acceptance notice and WIP paper submission deadline

~~May 28, 2012~~ June 25

Final written materials to be published in proceedings

July 23, 2012