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2013 IEEE International Symposium on Precision Clock Synchronization for Measurement, Control, and Communication

Plugfest September 22 – 24, 2013
Symposium September 25 – 27, 2013

Lemgo, Germany

Aim and objective: The objective of the symposium, to be held in Lemgo Germany, is to provide a forum for researchers and practitioners from industry, academia, and government involved in the area of **Precision Clock Synchronization and Distributed Time-based Applications**.

Symposium topics: We welcome paper submissions on all areas of precision clock synchronization and distributed time-based applications. Example topics of interest include but are not limited to:

Distributed applications based on synchronized clocks:

- Software and hardware architecture*
- Time-based programming models*
- Design environments and tools*
- Distributed algorithms using or based on synchronized clocks*
- Robustness of distributed time-based systems*
- Fault tolerance in distributed time-based systems*
- Security in distributed time-based systems*
- Application requirements studies*
- Case studies and field experience*
- Analytic, modelling, and simulation studies*
- Time-based cyber-physical systems*
- Time synchronization for robotics and control*
- Time synchronization for cloud computing infrastructure*
- IaaS Data Plane & PaaS Control/Management Plane*

Clock synchronization technology:

- Design, usage and research concerning IEEE 1588*
- Systems with heterogeneous synchronization technologies*
- Synchronization performance test and evaluation tools*
- Analytic, modelling, and simulation studies*
- Clock system management*
- Timing security and robustness*
- Synchronization over gigabit, fibre, and wireless networks*
- Local and wide-area synchronization*
- Servo design for slave clocks*
- Device design and application support*
- Conformance testing and system integration issues*
- Time and frequency distribution*
- Work-In-Progress reports*

Plugfest: The Plugfest is an opportunity to test and demonstrate device and system interoperability. Implementers are invited to cooperate with others and address compatibility issues in order to demonstrate system-wide clock synchronization and operation of protocols across a wide spectrum of devices. Prospective participants in the Plugfest should submit a letter of intent to participate, and an abstract of one page or less describing the devices, applications and the technical issues illustrated. Submission details are available at: <http://www.ispcs.org>.

Exhibitions: Promotional partner companies are invited to exhibit their products/prototypes. Exhibition space will be provided during the conference, allowing discussions with partners during the breaks. For details on becoming a promotional partner see: <http://www.ispcs.org>.

Submission of Papers: Authors should submit a six-page original manuscript. Templates and online submission of papers will be available at: <http://www.ispcs.org>. Students are welcome to submit papers.

Work in Progress Papers: Work in Progress (WIP) papers will be reviewed and presented in a poster session.

Paper Acceptance: All papers will be peer-reviewed. Each accepted paper must be presented at the conference by one of the authors. The final manuscript must be accompanied by the registration form and proof of payment. All conference and Plugfest attendees, including authors and session chairpersons, must pay the conference registration fee and their own travel expenses.

Final Papers: Final papers are limited to **six pages or less in IEEE double column format**. Full papers are accepted in IEEE compliant PDF format. All full papers will be published in the conference proceedings in CD and on IEEE Xplore.

Deadline for submission of regular papers

~~April 15, 2013~~ extended: **May 13, 2013**

Deadline for Plugfest registration

~~May 13, 2013~~ extended: **June 14, 2013**

Acceptance notice and WIP paper submission deadline

~~May 27, 2013~~ extended: **June 27, 2013**

Final written materials to be published in proceedings

July 22, 2013

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