2014 IEEE International Symposium on Precision Clock Synchronization for Measurement, Control, and Communication

Plugfest September 21 – 23, 2014
Symposium September 24 – 26, 2014

Austin, Texas, USA

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Aim and objective: The objective of the symposium, to be held in Austin, Texas, USA, is to provide a forum for researchers and practitioners from industry, academia, and government involved in the area of Precision Clock Synchronization and Distributed Time-based Applications.

Symposium topics: We welcome paper submissions on all areas of precision clock synchronization and distributed time-based applications. Example topics of interest include but are not limited to:

Distributed applications based on synchronized clocks:	Clock synchronization technology:
Software and hardware architecture	Design, usage and research concerning IEEE 1588
Time-based programming models	Systems with heterogeneous synchronization technologies
Design environments and tools	Synchronization performance test and evaluation tools
Distributed algorithms using or based on synchronized clocks	Analytic, modelling, and simulation studies
Robustness of distributed time-based systems	Clock system management
Fault tolerance in distributed time-based systems	Timing security and robustness
Security in distributed time-based systems	Synchronization over gigabit, fibre, and wireless networks
Application requirements studies	Local and wide-area synchronization
Case studies and field experience	Servo design for slave clocks
Analytic, modelling, and simulation studies	Device design and application support
Time-based cyber-physical systems	Conformance testing and system integration issues
Time synchronization for robotics and control	Time and frequency distribution
Time synchronization for cloud computing infrastructure	Work-In-Progress reports
IaaS Data Plane & PaaS Control/Management Plane	

Plugfest: The Plugfest is an opportunity to test and demonstrate device and system interoperability. Implementers are invited to cooperate with others and address compatibility issues in order to demonstrate system-wide clock synchronization and operation of protocols across a wide spectrum of devices. Prospective participants in the Plugfest should fill out the Plugfest Registration Form and email the completed form to ispcs-plugfest@iiss.at. Submission details are available at: http://www.ispcs.org.

Exhibitions: Promotional partner companies are invited to exhibit their products/prototypes. Exhibition space will be provided during the conference, allowing discussions with partners during the breaks. For details on becoming a promotional partner see: http://www.ispcs.org.

Submission of Papers: Authors should submit a six-page original manuscript. Templates and online submission of papers will be available at: http://www.ispcs.org. Students are welcome to submit papers.

Work in Progress Papers: Work in Progress (WIP) papers will be reviewed and presented in a poster session.

Paper Acceptance: All papers will be peer-reviewed. Each accepted paper must be presented at the conference by one of the authors. The final manuscript must be accompanied by the registration form and proof of payment.

Final Papers: Final papers are limited to **six pages or less in IEEE double column format**. Full papers are accepted in IEEE compliant PDF format. All full papers will be published in the conference proceedings in CD and on IEEE Xplore.

Conference Fees: All conference and Plugfest attendees, including authors, promotional partners and session chairpersons, must pay the conference registration fee and their own travel expenses.

Deadline for submission of regular papers	~~April 21, 2014~~ extended: May 19, 2014
Deadline for Plugfest registration	May 12, 2014
Acceptance notice and WIP paper submission deadline	~~May 26, 2014~~ extended: June 30, 2014
Final written materials to be published in proceedings	July 21, 2014

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